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Department of Electronics & Communication Engineering

IV B.Tech II SEM A.Y - 2025 - 2026 BRANCH : ECE - A

PROJECT TITLES WITH GUIDES

S.NO	Reg.NO	Name of the Student	Batch.No	Name of the Guide	Title of the project
1	22MQ1A0423	KEERTHI MEGHANA	22EC01	Mr.M.Sateesh	Design & verification of vending machine using verilog and system verilog
2	22MQ1A0439	SARAKANAM RAJESH			
3	22MQ1A0410	CH I VENKATA RAMANA			
1	22MQ1A0429	M NAGA SAI GREESHMA	22EC02	Dr K.Murali Babu	AES -Box Hardware with Efficiency Improvement Based on Linear LMO using verilog
2	22MQ1A0433	MUDDINENI CHARAN KUMAR			
3	22MQ1A0414	DUSANAPUDI SOWJANYA			
1	22MQ1A0417	GANTYADA YESODA	22EC03	Mrs P.Jyothi	Design and verification of USART Serial communication
2	22MQ1A0413	DINTAKURTHI SAI KUMAR			
3	22MQ1A0445	THOTA KAVYA			
1	22MQ1A0441	SOMISETTI NAGA NAVYA SRI	22EC04	Dr R.Samba siva nayak	SRAM Cell design Using finfet for low power and delay
2	23MQ5A0406	GAMIDI YASASWIN KASI			
3	22MQ1A0420	GORIPARTHI SUPRAJA			
1	22MQ1A0403	BHOGADHI KAVITHA	22EC05	Mr J J Swaroop	Design and Performance analysis of 64- bit Hybrid adders using verilog
2	22MQ1A0430	MADIVADA ANJALI			
3	223C1A0404	RAJULAPATI VENKATA SIRISHA			
4	22MQ1A0425	KOLLURI BHANUPRAKASH			
1	23MQ5A0402	BUSAM NAGASHARMI	22EC06	Mrs K.Sowmya Sri	Design and implementation of FIR Filter using Verilog
2	23MQ5A0408	GUNUPUDI JEEVAN SAI			
3	22MQ1A0435	P CHANDRA ADITHYA			
4	22MQ1A0440	SHAIK IMRAN			

1	22MQ1A0421	JALLURI KOMALIKA	22EC07	Mr N.Nagaraju	Noise Reduction in Low Light spinal image using CNN-Based spinal NET approach
2	22MQ1A0448	Y KEERTHI ABHINAYA			
3	22MQ1A0418	GOPU PRAISE PRAVEEN			
4	22MQ1A0406	BUDDANA PAVAN SANDEEP			
1	22MQ1A0434	PATTAPU GAYATHRI	22EC08	Mr Y.R.K.Paramahamsa	Image-Based emotion recognition using deep neural network
2	22MQ1A0422	KLEELA DURGA PRASAD RAO			
3	22MQ1A0411	CHANDANA SAI KEERTHI			
4	22MQ1A0437	RAJA GUNA SEKSHARA SAMBI REDDY			
1	22MQ1A0443	TAMMIREDDY SIRISHA	22EC09	Mr D.Sridhar	Multi modal feature disentanglement and contribution estimation for multimodality image fusion
2	23MQ5A0410	KAGITHA HEMA			
3	22MQ1A0416	GAJULA MOKSHAGNA			
4	22MQ1A0432	MOPIDEVI PRUDHVI NARAYANA			
1	23MQ5A0407	GEDELA JAHNAVI	22EC10	Mrs L.J.N Sree Lakshmi	Low Power MAC Architecture Used in DSP application
2	22MQ1A0407	BUSAM PAVANI			
3	22MQ1A0409	CH NAGA VENKATA SAI MUKESH			
4	22MQ1A0426	K SH RAVINDRANATH TAGORE			
1	22MQ1A0424	KOLLATI VIDHYA	22EC11	Mrs G.N.P.Jyothi	Hybrid Deep Learning Architecture using attention- Driven analysis for optimal multi-scale speech processing
2	22MQ1A0402	BANDARU CHARISHMA KEERTHI			
3	22MQ1A0428	KUTTUVA NAGARJUNA			
4	22MQ1A0431	MARUBOINA NAGA SAI LOKESH			
1	23MQ5A0401	BOMMINEEDU PURNA SAI	22EC12	Mrs J S Deepika	Static random access memory using cadence virtuoso GPDK 90NM technology
2	22MQ1A0447	VAKA ANAND SKLE RAM			
3	22MQ1A0438	S HARI VENKATA SATYA SAIKUMAR			
4	22MQ1A0405	BOTLA BHARGAVI			

1	23MQ5A0404	DEEVI TEJO VAMSI KRISHNA	22EC13	Mr.K.G.V.Nageswara Rao	AI powered vision system for automated quality grading of agriculture proceduce using image recognition
2	22MQ1A0419	GORIPARTHI SRI LAKSHMI DURGA			
3	22MQ1A0444	THOPULA HEMA SRI			
4	22MQ1A0408	CHALAMALASETTY KIRAN			
1	22MQ1A0412	D NAGA VENKATA SWATHI	22EC14	Mrs.B. Sujatha	Design and implementation of AMBA AXI 4.0 Master fOR high-speed performance Soc
2	22MQ1A0449	YERRAMSETTY NIHARIKA SRI			
3	22MQ1A0427	KOVVURI VENKATA PADMAVATHI			
4	23MQ5A0409	GUTTI DONTHU NAGA SAHITHI			
1	22MQ1A0436	PUTTI BINDU PAVANI	22EC15	Dr A chandra Suresh	Android Based wifi Controlled Robot using Raspberry Pi
2	23MQ5A0403	DASARI NARENDRA SAI KUMAR			
3	22MQ1A0404	BODICHERLA BHANU SAI PRAKASH			
4	22MQ1A0401	ANUMUKONDA GANGA BHAVANI			

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IV B.Tech II SEM A.Y - 2025 - 2026 BRANCH : ECE -B

PROJECT TITLES WITH GUIDES

S.NO	Reg.No	Name of the Student	Batch .NO	Name of the Guide	Title of the project
1	22MQ1A0483	TOTA NAGA MANIKANTA	22EC16	Mrs S.Rajeswari	Adaptive DVFS and Power-Gated VLSI Techniques for Energy-Efficient Wireless Sensor Node Design
2	22MQ1A0464	G VEERA VENKATA SIVA SANKAR			
3	22MQ1A0469	KANCHARLAPALLI MAHALAKSHMI			
1	23MQ5A0412	K SRI NAGA VENKATA SUKANYA	22EC17	Mrs L.J.N.Sree Lakshmi	Design and verification of car parking management system using verilog
2	23MQ5A0418	LUKKA MANASA			
3	22MQ1A0467	JUNGA SANDHYA SREE			
4	22MQ1A0485	VENNA CHIRU LAKSHMI			
1	22MQ1A0466	JOGI SAI PUJITHA	22EC18	Mr Y.R.K.Paramahamsa	Manual & semi Autonoums weed identification Roboat
2	22MQ1A0450	A DEVI VENKATESWARAMMA			
3	23MQ5A0430	PUVVALA LAKSHMI DURGA			
4	22MQ1A0481	SIRAMDASU KUMAR			
1	23MQ5A0423	METLA SAI JYOTHI	22EC19	Mr N.Nagaraju	Wide Load range high current Accuracy Low drop out regulator with load tracking compensation and dual soft start for wireless Charging System
2	23MQ5A0415	KOLLIPARA SAI RAJESH			
3	22MQ1A0465	GUTLA SRIKANTH			
4	22MQ1A0453	B TEJASWI SAI MANIDEEP			
1	22MQ1A0472	KODURU VINODINI	22EC20	Ms R Tulasi	Face detection using image Processing
2	22MQ1A0457	CHAPALA HARITHA SAI			
3	23MQ5A0425	NERUSU NAGA SAI SRIVALLI			
4	22MQ1A0455	CHALADI KANAKADURGA			

1	23MQ5A0420	PUPPALA ENEESHA	22EC21	Mrs CH Amala	implementation of 12bit R-2R DAC using cadence virtuoso in GPDK [illegible] technology
2	22MQ1A0476	NAGISETTI AAKASH BABU			
3	23MQ5A0431	SAMUDRALA DINESH			
4	22MQ1A0486	Y LAVANYA CHARAN TEJA			
1	22MQ1A0462	GOPISETTY GAYATHRI	22EC22	Mrs J S Deepika	Compact Super wideband frequency diversity hexagonal shaped monopoie antenna wih Switchable rejection band
2	22MQ1A0456	CHANDANA KALYAN			
3	22MQ1A0480	SANKULA SAI RAJU			
4	23MQ5A0434	JOGI NAGA SIVA KRISHNA			
1	22MQ1A0458	CH LAKSHMI NARASIMHASWAMY	22EC23	Mr D Sridhar	Medical image Fusion on latent Low rank representation and hesitant fuzzy granular energy
2	23MQ5A0419	MAMILLAPALLI NAGA SAI VAMSI			
3	22MQ1A0474	M JUMA VENKTA NAGA SAI			
4	22MQ1A0482	SRIKAKULAPU CHETAN PAVAN			
1	23MQ5A0405	DUSANAPUDI POOJITHA	22EC24	Mrs P Jyothi	Raspberry pi- based real-time air pollution tracker
2	22MQ1A0451	AVULA SAI VENKATA HARIKA			
3	23MQ5A0413	KATAKAM JYOTHSNA			
4	23MQ5A0427	PAMARTHI VENKAT			
1	23MQ5A0411	GADE SRIHAS	22EC25	Mr B Sudhakar Rao	Smart Fire Detection System Using Raspberry Pi
2	23MQ5A0433	VEDANTAM KRISHNA TEJA			
3	22MQ1A0463	GUDIVADA SHARMILI KRISHNA			
1	23MQ5A0421	MATTA MOHAN PHANI KUMAR	22EC26	Mr CHN V Tirumala	IOT BASED Digital Notice Board Using Raspberry pi
2	[illegible]	[illegible]			
3	[illegible]	[illegible]			
4	[illegible]	[illegible]			

1	22MQ1A0473	KOPPANATHI LOKESH	22EC27	Mrs G Karuna	Design and verification of Asychronous FIFO
2	22MQ1A0470	KASA RENUKA			
3	22MQ1A0471	KATAKAM SIRISHA			
4	22MQ1A0484	V NAGA BHARGAVA KRISHNA			
1	22MQ1A0475	MOVVA VENKATA SUBHASHINI	22EC28	Mr J.J.Swaroop	IOT Based weather monitoring station using Raspberry pi
2	23MQ5A0428	PEDDAPALLI TANUJA			
3	22MQ1A0477	PALAPARTHI JAGADEESH			
4	23MQ5A0420	MANNEM MUTTESWARI			
1	23MQ5A0432	SOMU HARISH VENKATA MANIKANTA	22EC29	Mrs K.Sowmya Sri	Standard cells using cadence virtuoso in GPDK 90nm CMOS technology
2	23MQ5A0414	KATTULA VASU			
3	22MQ1A0478	PAMARTHI TEJASWINI			
4	22MQ1A0479	PARIM MOHITHA			
1	23MQ5A0424	NALAM DHEERAJ	22EC30	Dr A.chandra Suresh	ANN model-assisted Quad port UWB MIMO antenna with enhanced isolation for WLAN and sub-6 GHz wireless
2	22MQ1A0461	GOLI NAVYA			
3	23MQ5A0417	KURMA MAHENDRA			
4	23MQ5A0426	PALLA RAJESH			

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This is to certify that the thesis entitled "LOW POWER MAC ARCHITECTURE USED IN DSP APPLICATIONS" being submitted by

G.JAHNAVI	23MQ5A0407
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CH.N.V.S.MUKESH	22MQ1A0409
K.S.R.TAGORE	22MQ1A0426

In partial fulfillment of the requirements for the award of degree of B.Tech in Electronics and Communication Engineering from Jawaharlal Nehru Technological University Kakinada, Kakinada is a record of bonafide work carried out by them at Sri Vasavi Institute of Engineering & Technology

The results embodied in this project report have not been submitted to any other University or Institute for the award of any degree or diploma.

L J N Sreelakshmi

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This is to certify that the thesis entitled "**DESIGN AND VERIFICATION OF UART SERIAL COMMUNICATION**" being submitted by

G. YESHODA	**22MQ1A0417**
D. SAI KUMAR	**22MQ1A0413**
T. KAVYA	**22MQ1A0445**

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This is to certify that the thesis entitled "**ANN Model-assisted quad-port UWB MIMO antenna with enhanced isolation for WLAN and Sub-6 GHz wireless applications**" being submitted by

N.DHEERAJ	**23MQ5A0424**
G.NAVYA	**22MQ1A0461**
K.MAHENDRA	**23MQ5A0417**
P.RAJESH	**23MQ5A0426**

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This is to certify that the entitled **"SRAM CELL DESIGN USING FINFET FOR LOWPOWER AND DELAY"** Being submitted by

S. NAGA NAVYA SRI	**22MQ1A0441**
G. YASASWIN KASI	**23MQ5A0406**
G. SUPRAJA	**22MQ1A0420**

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This is to certify that the thesis entitled "Hybrid Deep Learning Architecture Using Attention-Driven Analysis for Optimal Multi-Scale Speech Processing" being submitted by

K.VIDHYA	**22MQ1A0424**
B.CHARISHMA KEERTHI	**22MQ1A0402**
K.NAGARJUNA	**22MQ1A0428**
M.N.S.LOKESH	**22MQ1A0431**

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This is to certify that the thesis entitled "**Design and implementation of AMBA AXI 4.0 Master for High – Speed Performance soC**" being submitted by

D. N V SWATHI	**22MQ1A0412**
Y. NIHARIKA SRI	**22MQ1A0449**
K. V PADMAVATHI	**22MQ1A0427**
G. D N SAHITHI	**23MQ5A0409**

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This is to certify that the thesis entitled "**FACE DETECTION USING IMAGE PROCESSING**" being submitted by

K. VINODINI	**22MQ1A0472**
CH. HARITHA SAI	**22MQ1A0457**
N. SRIVALLI	**23MQ5A0425**
CH. KANAKADURGA	**22MQ1A0455**

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This is to certify that the thesis entitled "AI-POWERED VISION SYSTEM FOR AUTOMATED QUALITY GRADING OF AGRICULTURAL PRODUCE USING IMAGE RECOGNITION" being submitted by

D.TEJO VAMSI KRISHNA	**23MQ5A0404**
G.SRI LAKSHMI DURGA	**22MQ1A0419**
T.HEMA SRI	**22MQ1A0444**
CH.KIRAN	**22MQ1A0408**

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This is to certify that the entitled "NOISE REDUCTION IN LOW LIGHT SPINAL IMAGES USING CNN BASED SPINAL NET APPROACH" Being submitted by

J. KOMALIKA	**22MQ1A0421**
Y. KEERTHI ABHINAYA	**22MQ1A0448**
G. PRAISE PRAVEEN	**22MQ1A0418**
B. PAVAN SANDEEP	**22MQ1A0406**

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This is to certify that the thesis entitled "Design and verification of asynchronous FIFO" being submitted by

K. LOKESH	**22MQ1A0473**
K. RENUKA	**22MQ1A0470**
K. SIRISHA	**22MQ1A0471**
V. NAGA BHARGAVA KRISHNA	**22MQ1A0484**

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This is to certify that the thesis entitled **"STANDARD CELLS USING CADENCE VIRTUOSO GPDK 90NM TECHNOLOGY"** being submitted by

S.HARISH VENKATA MANIKANTA	**23MQ5A0432**
K.VASU	**23MQ5A0414**
P.TEJASWINI	**22MQ1A0478**
P.MOHITHA	**22MQ1A0479**

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This is to certify that the thesis entitled **"ADAPTIVE DVFS AND POWER-GATED VLSI TECHNIQUES FOR ENERGY-EFFICIENT WIRELESS SENSOR NODE DESIGN"** being submitted by

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This is to certify that the thesis entitled "WIDE LOAD RANGE, HIGH CURRENT ACCURACY LOW DROP OUT REGULATOR WITH LOAD TRACKING COMPENSATION AND DUAL SOFT START FOR WIRELESS CHARGING SYSTEM" being submitted by

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This is to certify that the thesis entitled "**DESIGN AND VERIFICATION OF CAR PARKING MANAGEMENT SYSTEM USING VERILOG**" being submitted by

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