



Engineering Mind

SRI VASAVI INSTITUTE OF ENGINEERING & TECHNOLOGY

An Autonomous Institute

(Approved by AICTE, New Delhi & Permanently Affiliated to JNTUK Kakinada)
Accredited by NBA (Mech, ECE, CSE) & NAAC, An ISO 9001:2015 Certified Institute
Nandamuru, Pedana Mandal, Krishna Dist – 521369.



Date : 10-12-2025

M.TECH III SEMESTER (R23) REGULAR/SUPPLEMENTARY END EXAMINATIONS, JANUARY - 2026

TIME TABLE

TIME : 10.00 AM TO 01.00 PM

BRANCH	07-01-2026 (WEDNESDAY)	09-01-2026 (FRIDAY)
ECE (VLSI System Design - 61)	DIGITAL SYSTEM DESIGN & VERIFICATION (M23EC31B)	INDUSTRIAL SAFETY (M23EC32B)
CSE (Computer Science & Engineering - 58)	SERVICE ORIENTED ARCHITECTURE(SOA) (M23CS31B)	-----

NOTE:

- Any Omissions or clashes in this time table may please be informed to the controller of examination immediately.
- The HOD's are requested to inform the Controller of Examination any other substitute subjects that are not included in the above time table immediately.


12/12/25

Controller of Examinations



Principal

Copy to:

- 1) Chairman, Secretary, Correspondent,
- 2) Executive Directors,
- 3) Controller of Examinations,
- 4) HODs – ECE & CSE,
- 5) Website, Main Notice Board, Student's Notice Board, Circular file & Exam Cell