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Department of Electronics and communication Engineering

Details of Faculty Publications, Academic Year: 2024-25

S. No	Author Name	Title of the Paper	Journal Name & Publisher Name	Vol. No., Issue No., Page No. & Date	ISBN/ISSN No (On Line & Print)	Publication link
1	Dr. A. Chandra Suresh	Design and Implementation of Smart Mirror Using Raspberry Pi	IJREEIE	Volume 14, Issue 4, 1102- 1106, April 2025	e-ISSN: 2278-8875 p-ISSN: 2320-3765	https://ijareeie.com/upload/2025/april/17_Design.pdf
2.	Dr. A. Chandra Suresh	Detection of Exam Cheating through Surveillance using Face Detection with AI	IJRSET	Volume 14, Issue 4, 8750- 8753, April 2025	e-ISSN: 2319-8753 p-ISSN: 2347-6710	https://www.ijrset.com/upload/2025/april/414_Detection.pdf
3.	Dr. A. Chandra Suresh	Model analysis-based ultrawideband 4x4 MIMO antenna with flower configuration	International Journal of Electronics and communication	27 January 2025 AEUE - 192 (2025) 155685	SCI	https://www.sciencedirect.com/science/article/pii/S1434841250002637?via=ihub

4.	Dr. Shaik Khader Zelani	Optimized FIR Filter Architecture with High Speed Multiplier and Three Operand Adder	IJIRSET	Volume 14, Issue 4, 8135- 8138, April 2025	e-ISSN: 2319-8753 p-ISSN: 2347-6710	https://www.ijirset.com/upload/2025/april/331_Optimized.pdf
5.	Dr. Shaik Khader Zelani	System Verilog Test Bench Architecture For VLSI Chip Design Verification Of A 64-Bit ALU	IJIRSET	Volume 14, Issue 4, 1107- 1110, April 2025	e-ISSN: 2278-8875 p-ISSN: 2320-3765	https://ijareeie.com/upload/2025/april/18_Sym.pdf
6.	Dr. Shaik Khader Zelani	Energy efficient ant routing algorithm for optimized path selection network longevity in wireless sensor network.	Telecommunication and Radio Engineering	1943-6009, November 2024	SCOOPERS	https://www.dl.begellhouse.com/journals/0632a9d54950b268,430cdfe07ac9aced,0073a74627f813c2.html
7.	Mrs.Karuna gone	Optimized Approximate 7:2 Compressor for Fast and Low-Power Multiplication	IJIRCCE	Volume 13, Issue 4, April 2025, 9104-9108	e-ISSN: 2320-9801 p-ISSN: 2320-9798	https://ijircce.com/admin/main/storage/app/pdf/CMczy1KbWkHSKFU sELk kbECVP48BhlpMuT46ESrN.pdf

8.	Mr. N. Naga Raju	Security Based Biometric Voting Machine using Xilinx Tool	IJRCCE	Volume 13, Issue 4, April 2025, 9109-9112	e-ISSN: 2320-9801 p-ISSN: 2320-9798	https://ijircce.com/admin/main/storage/app/pdf/iyxCUdVifbUGA6ABYgHDwZiIRuAXfx7qe595NKeW.pdf
9.	Mrs.K. Sowmya Sri	Advanced Anti-Lock Braking System(ABS) using Raspberry Pico	IJRSET	Volume 14, Issue 4, 8139- 8142, April 2025	e-ISSN: 2319-8753 p-ISSN: 2347-6710	https://www.ijrset.com/upload/2025/april/332 Advanced.pdf
10.	Mrs. L.J.N. Sree Lakshmi	Design & Analysis of 32-Bit Vedic Multiplier Using Parallel Prefix Adder for Low Power Applications	IJRSET	Volume 14, Issue 4, 1098- 1101, April 2025	e-ISSN: 2278-8875 p-ISSN: 2320-3765	https://ijareeie.com/upload/2025/april/16 Design.pdf
11.	Mr. D.Sridhar	Decoder Reduction Approximation Scheme for 16-Bit Accurate Booth Multipliers	IJRCCE	Volume 13, Issue 4, April 2025, 9278-9285	e-ISSN: 2320-9801 p-ISSN: 2320-9798	https://ijircce.com/admin/main/storage/app/pdf/BB8jAD5GgzavLvXrj4RX6cgoDJkuBgfdOb

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12.	Mrs.J.S. Deepika	Design an Efficient Braun Multiplier Using KSA Based Pass Transistor Logic Using 22	IJIRSET	Volume 14, Issue 4, 8143- 8147, April 2025	e-ISSN: 2319-8753 p-ISSN: 2347-6710	https://www.ijirset.com/upload/2025/april/332/Design.pdf
13.	Mr. J.Jyothi Swaroop	Design and Implementation of a 32-Bit MIPS RISC Processor with Flexible 7 Stage Pipelining and Dynamic Thermal Control with Verilog System	IJIRSET	Volume 14, Issue 4, 8197- 8200, April 2025	e-ISSN: 2319-8753 p-ISSN: 2347-6710	https://www.ijirset.com/upload/2025/april/334/Design.pdf
14.	Mrs. Y.R.K.Paramahansa	Design an Area Efficient Kogge Stone Adder Using Pass Transistor Logic of 22nm	IJIRSET	Volume 14, Issue 4, 8201- 8204, April 2025	e-ISSN: 2319-8753 p-ISSN: 2347-6710	https://www.ijirset.com/upload/2025/april/361/Design.pdf
15.	Ms. R. Tulasi	Empowering Women with Technology: A GPS and GSM Integrated Approach to Personal Safety	IJIRSET	Volume 14, Issue 4, 1111- 1114, April 2025	e-ISSN: 2278-8875 p-ISSN: 2320-3765	https://ijareeie.com/upload/2025/april/19/Empowering.pdf

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16.	Mrs.P.Jyothi	Plant Disease Detection and Natural Pesticide Recommendation Using OpenCV and TensorFlow	IJIRCCE	Volume 13, Issue 4, April 2025, 9313-9316	e-ISSN: 2320-9801 p-ISSN: 2320-9798	https://ccea.com/admin/n/storag/app/pOGP55UdHA6FA3ZGcTydLfOI6LhW1bx.pdf
17.	Ms. R. Tulasi	AI Powered Face Recognition for Secure & Scalable Voting System	IJIRCCE	Volume 13, Issue 4, April 2025, 9317-9320	e-ISSN: 2320-9801 p-ISSN: 2320-9798	https://ccea.com/admin/n/storag/app/pf9ozBo15Bn2ElSaTlx5RMYBMZI2xl88zZCdf
18.	Mrs.J.S. Deepika	Crowd Density Estimation and Security Risk Monitoring in Real Time Using YOLOv8	IJIRCCE	Volume 13, Issue 4, April 2025, 9321-9325	e-ISSN: 2320-9801 p-ISSN: 2320-9798	https://ccea.com/admin/n/storag/app/pceAU3U5sp4GypRI6FFGtXs27H2sqtrQL7pdf

19.	Mr.N.Nagaraju	Design and Analysis of a Low-Power 5:2 Compressor based Approximate Multiplier with Reconfigurable Truncation	IJIRCE	Volume 13, Issue 4, April 2025,9326-9334	e-ISSN: 2320-9801 p-ISSN: 2320-9798	https://www.ijirce.com/admin/n/stora/app/pdf/JVmknhJ5f2QErV2OdgaaNmcDbHuigta.pdf
20.	Mr. J.Jyothi Swaroop	Performance Evaluation of 6T,7T,8T & 9T SRAM Cell Topologies at 22nm Technology Node	IJIRCE	Volume 13, Issue 4, April 2025,9290-9296	e-ISSN: 2320-9801 p-ISSN: 2320-9798	https://www.ijirce.com/admin/m/storag/app/pdf/CJYG9eIXeSlw6vcryVi57QlscBrDWpzWcEXk.pdf
21.	Mrs. Y.R.K.Paramahamsa	Design of Approximate Multipliers using Approximate Higher-Order Compressors	IJIRSET	Volume 14, Issue 4, 8740- 8246, April 2025	e-ISSN: 2319-8753 p-ISSN: 2347-6710	http://www.ijirset.com/upload/2025/april/412Design.pdf
22.	Mrs.L.J.N.Sree Lakshmi	Human Based Deep Fake Video Detection and Estimation using Region Convolutional Neural Networks	IJIRSET	Volume 14, Issue 4, 8529- 8533, April 2025	e-ISSN: 2319-8753 p-ISSN: 2347-6710	https://www.ijirset.com/upload/2025/april/393HUMAN.p

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23.	Mrs.Karuna gone	Design and Verification of Custom RISC Processor Architecture using Verilog and Testing - Validation with System Verilog System	IJIRSET	Volume 14, Issue 4, 8201- 8205, April 2025	e-ISSN: 2319-8753 p-ISSN: 2347-6710	https://www.ijirset.com/upload/2025/april/335_Design.pdf
24.	Mrs.K.Sowmya sri	Design of AMBA APB Bridge for Two High Speed Peripheral Communication	IJIRSET	Volume 14, Issue 4, 8747- 8749, April 2025	e-ISSN: 2319-8753 p-ISSN: 2347-6710	https://www.ijirset.com/upload/2025/april/413_Design.pdf
25.	Mrs.P.Jyothi	WalkPower: Transforming Footstep Motion into Electrical Energy with Piezoelectric Sensors Using Raspberry Pi Pico	IJIRCCE	Volume 13, Issue 4, April 2025, 9305- 9309	e-ISSN: 2320-9801 p-ISSN: 2320-9798	https://ijircce.com/admin/main/storage/app/pdf/abN8PULTNm5vYppZ7pBAoqTfN4kMHVhUx7bQXin.pdf
26.	K.Sai Sudheer	Intelligent Parking Management System Using Infrared Sensor Networks	IJIRCCE	Volume 13, Issue 4, April 2025, 9310- 9312	e-ISSN: 2320-9801 p-ISSN: 2320-9798	https://ijircce.com/admin/main/storage/app/pdf/1tceVHiYeL86CFpzFKWwx93Y

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27.	S. Rajeswari	Integrated Monitoring and Alerting System for Enhanced Mine Worker Safety	IJIRCCE	Volume 13, Issue 4, April 2025 9335-9339	e-ISSN: 2320-9801 p-ISSN: 2320-9798	https://ijircce.com/admin/main/storage/app/pdf/orfTYAQvNbo4JtbwZ2ODFLWsFQEP7HHRHLOKO.pdf


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