



SRI VASAVI INSTITUTE OF ENGINEERING & TECHNOLOGY

An Autonomous Institute

(Approved by AICTE, New Delhi & Permanently Affiliated to JNTUK Kakinada)
Accredited by NBA (Mech, ECE, CSE) & NAAC, An ISO 9001:2015 Certified Institute
Nandamuru, Pedana Mandal, Krishna Dist – 521369.



Date : 03-10-2024

M.TECH III SEMESTER (R23 REGULATION) I MID EXAMINATIONS, OCTOBER - 2024

TIME TABLE

TIME : 10.00 AM TO 12.00 PM

BRANCH	07-10-2024 (MONDAY)	08-10-2024 (TUESDAY)
ECE (VLSI System Design - 61)	DIGITAL SYSTEM DESIGN & VERIFICATION (M23EC31B)	COST MANAGEMENT OF ENGINEERING PROJECTS (M23EC32D)

NOTE:

- Any Omissions or clashes in this time table may please be informed to the controller of examination immediately.
- The HOD's are requested to inform the Controller of Examination any other substitute subjects that are not included in the above time table immediately.

Amrta 3/10/24
Controller of Examinations

Principal
Principal

Copy to:

- Chairman, Secretary, Correspondent,
- Executive Directors,
- Controller of Examinations,
- HOD – ECE,
- Main Notice Board, Student's Notice Board, Circular file & Exam Cell