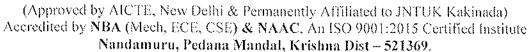


SRI VASAVI (. STITUTE OF ENGINEERING STECHNOLOGY

An Autonomous Institute





Date: 29-01-2024

M.TECH I SEMESTER (R23 REGULATION) END EXAMINATIONS, FEBRUARY - 2024 TIME TABLE

TIME: 10.00 AM TO 01.00 PM

BRANCH	05-02-2024	07-02-2024	09-02-2024	12-02-2024	14-02-2024
	(MONDAY)	(WEDNESDAY)	(FRIDAY)	(MONDAY)	(WEDNESDAY)
ECE (VLSI System-Design - 61)	CMOS ANALOG IC DESIGN (M23EC11)	CMOS DIGITAL IC DESIGN (M23EC12)	Elective – I VLSI TECHNOLOGY (M23EC13A)	Elective II DEVICE MODELING (M23EC14A)	RESEARCH METHODOLOGY AND IPR (M23EC15)

NOTE:

- i. Any Omissions or clashes in this time table may please be informed to the controller of examination immediately.
- ii. The HOD's are requested to inform the Controller of Examination any other substitute subjects that are not included in the above time table immediately.

Controller of Examinations

Principal

Copy to:

- 1) Chairman, Secretary, Correspondent,
- 2) Executive Directors,
- 3) Controller of Examinations,
- 4) Hod ECE,
- 5) Main Notice Board, Student's Notice Board, Circular file & Exam Cell